

Abstract clocks for the N-synchronous model

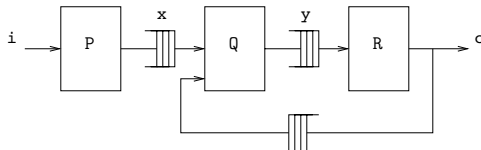
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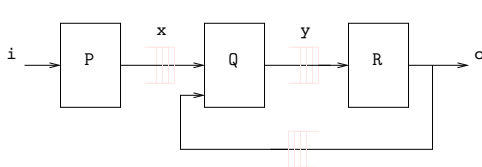
SYNCHRON - November 26th, 2007

Programming Kahn Networks

Kahn Networks :



Synchronous Dataflow Languages (Lustre, Signal, Lucid Sychrone) allow to program synchronous kahn networks.



```
let node my_skn i =  
  let x = P i  
  and y = Q x (0 fby o)  
  and o = R y in  
  o
```

Introduction

The N-synchronous model
Constraints resolution by abstraction of periods
Conclusion

Motivation

The synchronous model

The synchronous model with periodic clocks

Synchronous Dataflow Languages

flow	value	clock type
<i>nat</i>	0 1 2 3 4 5 ...	<i>base</i>
<i>prime</i>	1 3 5 7 11 13 ...	<i>base</i>
<i>twoOvFour</i>	t t f f t t ...	<i>base</i>

Synchronous Dataflow Languages

flow	value	clock type
<i>nat</i>	0 1 2 3 4 5 ...	<i>base</i>
<i>prime</i>	1 3 5 7 11 13 ...	<i>base</i>
<i>twoOvFour</i>	t t f f t t ...	<i>base</i>
<i>x = nat when twoOvFour</i>	0 1 4 5 ...	<i>base on twoOvFour</i>
<i>y = prime when twoOvFour</i>	1 3 11 13 ...	<i>base on twoOvFour</i>
<i>z = x + y</i>	1 4 15 18 ...	<i>base on twoOvFour</i>
<i>oneOvTwo</i>	t f t f ...	<i>base on twoOvFour</i>
<i>x when oneOvTwo</i>	0 4 ...	<i>base on twoOvFour on oneOvTwo</i>
<i>oneOvFour</i>	t f f f t f ...	<i>base</i>
<i>prime when oneOvFour</i>	1 11 ...	<i>base on oneOvFour</i>

Clocks can be arbitrarily complex : let $\text{clock } c1 = (x \leq 0) \& b$
equality of clocks = syntactic equality

Synchronous model

- ▶ clocks = boolean flows, arbitrarily complex
- ▶ composition : syntactically equal clocks, structural unification

ex.: $\alpha_1 \text{ on } c_1 \equiv \alpha_2 \text{ on } c_2 \text{ on } c_3$

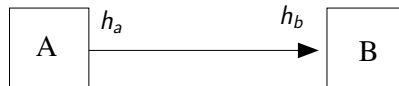
$$\Leftrightarrow (c_1 \stackrel{\text{synt}}{=} c_3) \wedge (\alpha_1 \equiv \alpha_2 \text{ on } c_2)$$

Synchronous Model with periodic clocks

- ▶ clocks = ultimately periodic boolean flows
- ▶ composition : equal clocks, computed unification.
ex.: $\alpha_1 \text{ on } (10) \equiv \alpha_2 \text{ on } (1000)$
 $\Leftrightarrow (\alpha_1 = \alpha'_1 \text{ on } (1100)) \wedge (\alpha'_1 \equiv \alpha_2)$
- ▶ Particular case $0^d(10^n)$: affine clocks (I. Smarandache)

Relaxing the synchronous condition

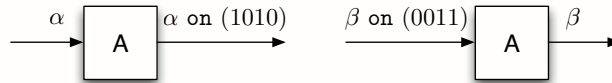
Goal : more flexibility in the composition of flows



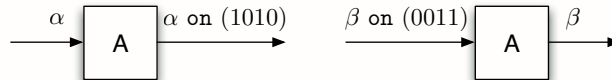
- ▶ 0-synchronous : $h_a = h_b$, no buffers.
- ▶ n-synchronous : $h_a <: h_b$, buffer of size n .
- ▶ inference of the buffers size
- ▶ automatic translation to a synchronous kahn network

Example

Synchronous Model:

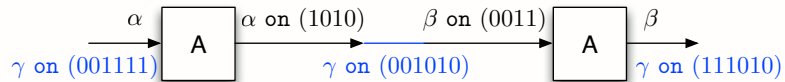


N-Synchronous model:

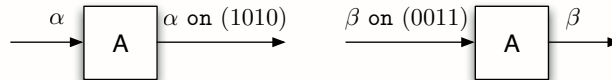


Example

Synchronous Model: unification

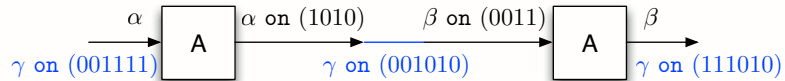


N-Synchronous model:

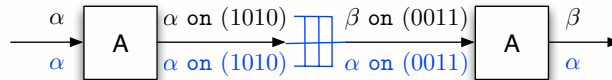


Example

Synchronous Model: unification

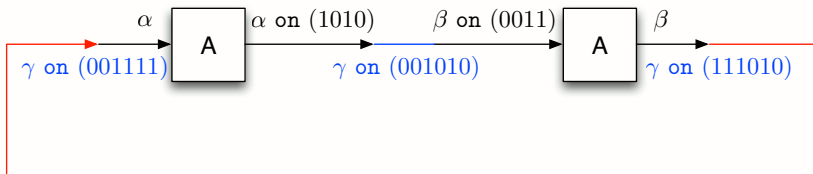


N-Synchronous model: sub-typing

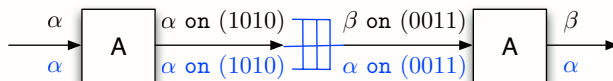


Example

Synchronous Model: the program is rejected

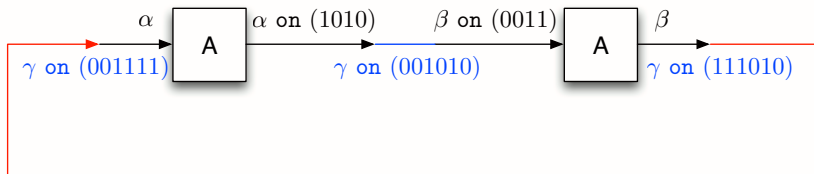


N-Synchronous model: sub-typing

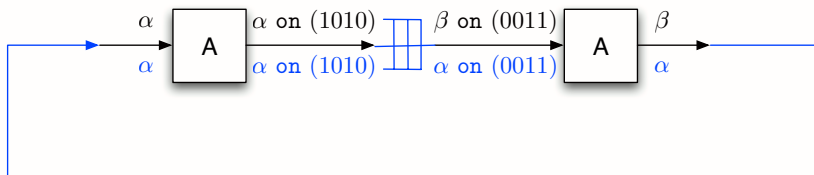


Example

Synchronous Model: the program is rejected



N-Synchronous model: the program is accepted



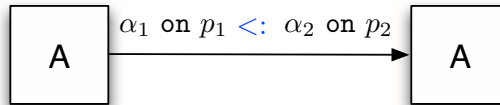
Sub-typing Relation

As we use periodic words,
we can compare the clocks:

- ▶ **synchronizability**: $p_1 \bowtie p_2$
“ p_1 as in average as many ones as p_2 ”
- ▶ **precedence** : $p_1 \preceq p_2$
“the 1 of p_1 arrive before those of p_2 ”

sub-typing relation: $p_1 <: p_2 \Leftrightarrow p_1 \bowtie p_2 \wedge p_1 \preceq p_2$

Clock calculus



- ▶ We collect a set of constraints of the form
 $S : \{\alpha_i \text{ on } p_i <: \alpha_j \text{ on } p_j\}_{i,j}$
- ▶ Finding a valid instantiation of variables :
Complete Resolution :
 $S \Leftrightarrow S' \Leftrightarrow \dots \Leftrightarrow \{ \}$
Correct Resolution :
 $S \Leftarrow S' \Leftarrow \dots \Leftarrow \{ \}$

Difficulty 1 : no structural simplification of on

$$\alpha \text{ on } p <: \beta \text{ on } p \not\Rightarrow \alpha <: \beta$$

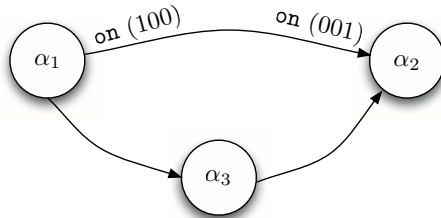
for ex. :

α	on (01)	<:	β	on (01)
γ on (0110)	on (01)	<:	γ on (1001)	on (01)
γ on (0110)		$\nless$:	γ on (1001)	

⇒ The α_n in the constraints must be computed

$$\begin{array}{l} \alpha \text{ on } w \quad \text{on } w_1 \dots \text{on } w_n <: \beta \text{ on } w' \quad \text{on } w_1 \dots \text{on } w_n \\ \not\Rightarrow \alpha \text{ on } w <: \beta \text{ on } w' \end{array}$$

Difficulty 2 : we cannot always work locally



$$\left\{ \begin{array}{lcl} \alpha_1 \text{ on } (100) & <: & \alpha_2 \text{ on } (001) \\ \alpha_1 & <: & \alpha_3 \\ \alpha_3 & <: & \alpha_2 \end{array} \right\}$$

Here the constraint by transitivity is more difficult to satisfy than the direct constraint.

NB : We cannot always compare 2 constraints

Constraints resolution by the abstraction of periods

We approximate a period p by $[d, D] + (r)$.

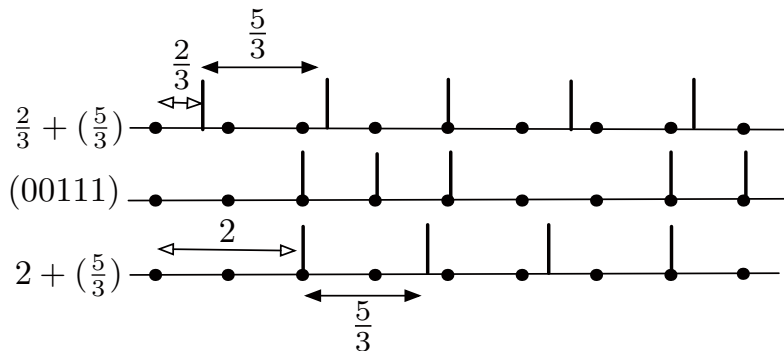
r = rate of p

d, D = bounding of the ones indexes in p

Definition (abstraction)

$abs(p) = [d, D] + (r) \Leftrightarrow \forall i \geq 0, d + r \times i \leq [p]_i \leq D + r \times i$
with $d, D \in \mathbb{Q}$, d maximal and D minimal.

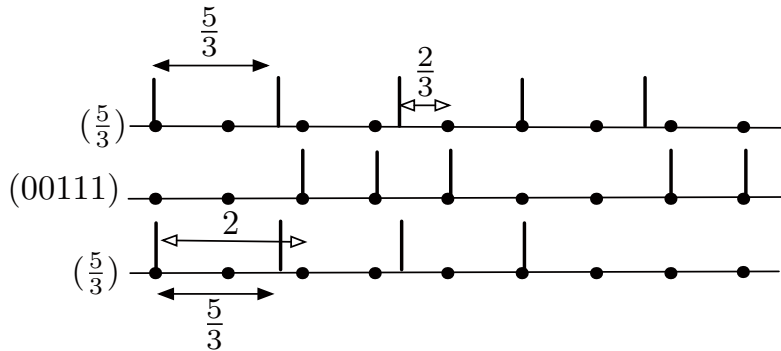
example: $abs((00111)) = [\frac{2}{3}, 2] + (\frac{5}{3})$



$\frac{2}{3} + (\frac{5}{3}) <: (00111) <: 2 + (\frac{5}{3})$

Abstraction of an infinite binary word

$$\text{abs}((00111)) = [\frac{2}{3}, 2] + (\frac{5}{3})$$



Concretization of an abstract period

Definition (concretization)

$$\text{concr}([d, D] + (r)) = \{w, \forall i \geq 0, d + r \times i \leq [w]_i \leq D + r \times i\}$$

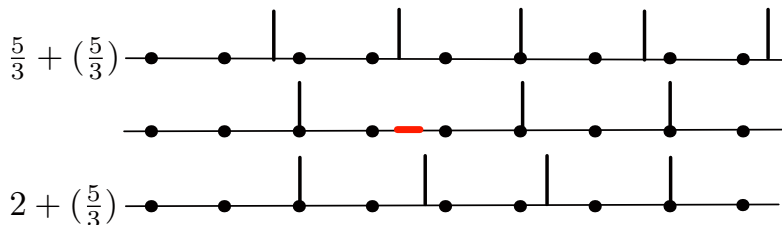
"All words that come between $d+(r)$ and $D+(r)$ "

Infimum and supremum of the concretization set, if it's not empty:

$$w_{\text{inf}} = \text{inf}(\text{concr}(pa)) \Leftrightarrow \forall i \geq 0, [w_{\text{inf}}]_i = \lceil d + r \times i \rceil$$

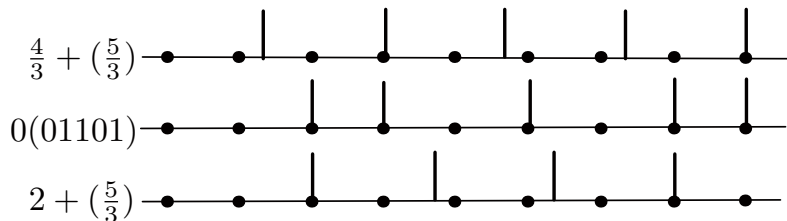
$$w_{\text{sup}} = \text{sup}(\text{concr}(pa)) \Leftrightarrow \forall i \geq 0, [w_{\text{sup}}]_i = \lfloor D + r \times i \rfloor$$

$$D - d < 1 - \frac{1}{n} \Rightarrow \text{concr}([d, D] + (\frac{m}{n})) = \emptyset$$



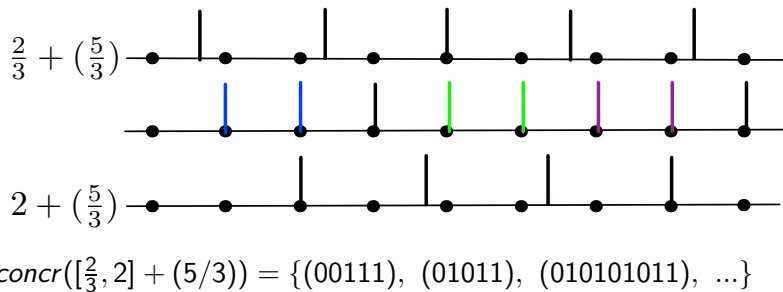
$$\text{concr}([\frac{5}{3}, 2] + (5/3)) = \{\}$$

$$D - d = 1 - \frac{1}{n} \Rightarrow \text{concr}([d, D] + (\frac{m}{n})) = \{w\}$$



$$\text{concr}([\frac{4}{3}, 2] + (5/3)) = \{0(01101)\}$$

$$D - d > 1 - \frac{1}{n} \Rightarrow \text{concr}([d, D] + (\frac{m}{n})) = \{w_1, w_2, \dots, w_k, \dots\}$$



Abstraction of the constraint system

We have : $C : \{\alpha_i \text{ on } p_i <: \alpha_j \text{ on } p_j\}_{i,j}$

$\alpha_i = \text{base on } x_i$ and $\alpha_j = \text{base on } x_j$.

We have to find x_i, x_j such that : $\{x_i \text{ on } p_i <: x_j \text{ on } p_j\}_{i,j}$

We abstract :

$C^\sim : \{\text{abs}(x_i) \text{ on}^\sim \text{abs}(p_i) <:\sim \text{abs}(x_j) \text{ on}^\sim \text{abs}(p_j)\}_{i,j}$

- ▶ $\text{on}^\sim$ operator ?
- ▶ $<:\sim$ relation ?
- ▶ resolution of abstract constraints ?

$\text{on}^\sim$ operator

$$[d_1, D_1] + (r_1) \text{ on}^\sim [d_2, D_2] + (r_2) = [d_{12}, D_{12}] + (r_1 \times r_2)$$

with :

$$d_{12} = d_1 + d_2 \times r_1$$

$$D_{12} = D_1 + D_2 \times r_1$$

property: $p_1 \text{ on } p_2 \in \text{concr}(\text{abs}(p_1) \text{ on}^\sim \text{abs}(p_2))$

Abstraction of a period:

$$\text{abs}(w_1 \text{ on } \dots \text{ on } w_k) = \text{abs}(w_1) \text{ on}^\sim \dots \text{ on}^\sim \text{abs}(w_k)$$

$<:\sim$ relation

► Definition (synchronizability)

$$pa_1 \bowtie^\sim pa_2 \Leftrightarrow \forall p_1 \in \text{concr}(pa_1), p_2 \in \text{concr}(pa_2), p_1 \bowtie p_2$$

synchronizability test :

$$[d_1, D_1] + (r_1) \bowtie^\sim [d_2, D_2] + (r_2) \Leftrightarrow r_1 = r_2$$

$$\textbf{property : } \text{abs}(p_1) \bowtie^\sim \text{abs}(p_2) \Leftrightarrow p_1 \bowtie p_2$$

$<:\sim$ relation

► Definition (precedence)

$$pa_1 \preceq^\sim pa_2 \Leftrightarrow \forall p_1 \in \text{concr}(pa_1), p_2 \in \text{concr}(pa_2), p_1 \preceq p_2$$

precedence test :

$$\begin{aligned} pa_1 \preceq^\sim pa_2 &\Leftrightarrow \sup(\text{concr}(pa_1)) \preceq \inf(\text{concr}(pa_2)) \\ &\Leftrightarrow \forall i \geq 0, \lfloor r_1 \times i + D_1 \rfloor \leq \lceil r_2 \times i + d_2 \rceil \end{aligned}$$

particular case :

$$\text{if } r_1 = r_2 = \frac{m}{n} \text{ then } pa_1 \preceq^\sim pa_2 \Leftrightarrow D_1 - d_2 \leq 1 - \frac{1}{n}$$

property : $\text{abs}(p_1) \preceq^\sim \text{abs}(p_2) \Rightarrow p_1 \preceq p_2$

$$\text{Sat}(C^\sim) \Rightarrow \text{Sat}(C)$$

Constraints resolution

$$C^{\sim} : \{abs(x_i) \text{ on }^{\sim} abs(p_i) <:^{\sim} abs(x_j) \text{ on }^{\sim} abs(p_j)\}_{i,j}$$

$$C^{\sim} : \{xa_i \text{ on }^{\sim} pa_i <:^{\sim} xa_j \text{ on }^{\sim} pa_j\}_{i,j} \text{ with } \{concr(xa_k \neq \emptyset)\}_k$$

after the computation of the $\text{on}^{\sim}$:

$$C^{\sim} : \{(r_i) + [d_i, D_i] <:^{\sim} (r_j) + [d_j, D_j]\}_{i,j}$$

- ▶ a set of constraints on rates $C_{rates}^{\sim} : \{r_i = r_j\}_{i,j}$
- ▶ a set of constraints on delays
 $C_{delays}^{\sim} : \{D_i - d_j \leq c_{ij}\}_{i,j} + \{D_k - d_k \leq c_k\}$

Conclusion

- ▶ All valid systems without cycles are accepted
- ▶ Some valid systems with cycles are rejected.
- + priority to the bufferization
(we always synchronize all the clocks on the slower one)
- + dealing with less regular clocks
Type checking guarantee: “ if the clocks correspond to their specification, then the program is n-synchronous”

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