#### UML & FM 2010 in Shanghai

GENERATING COMPOSITE BEHAVIOR OF EMBEDDED SOFTWARE COMPONENTS BASED ON UML BEHAVIORAL MODEL AND PROCESS ALGEBRA

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- Introduction
- Background
  - TRoS and ACSR
- TRoS || ACSR for Embedded Software Components
- Specifying Real-time Embedded Applications in TRoS || ACSR
- Conclusions

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#### PLC for NPP



#### STATEMATE Statecharts

- It is widely used to model not only HW but also embedded SW,
- There are plenty of analyzing tools, such as model checking, simulation, and test

# Model of pCOS



## Semaphore Model in SC



# Semaphore in ACSR

#### SemMng\_B\_SEM\_1

- = {}:SemMng\_B\_SEM\_1
- + (OSSemCreate\_B\_SEM\_1,1).B\_SEM\_1\_Pend ;

#### B\_SEM\_1\_Pend

- = {}:B\_SEM\_1\_Pend
- + (OSSemPend\_SEM\_1,1).B\_SEM\_1\_Post
- + (OSSemDelete\_B\_SEM\_1,1).SemMng\_B\_SEM\_1;

#### B\_SEM\_1\_Post

- = {}:B\_SEM\_1\_Post
- + (OSSemPost\_SEM\_1,1).('OSSched,1).B\_SEM\_1\_Pend

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## TRoS

- TRoS = Statecharts + Timed Actions
- The timed action specifies timed behaviors constrained by the prioritized use of resources.
- It can be transformed directly from ACSR in easy way by rules we have defined.

#### **Timed Action**

A ::= {S}<sup>n</sup> | < S ><sup>n</sup>
S ::= ε | (r,ve),S

# Example



#### Algorithm 1 TRoSexec

1:	procedure $\operatorname{TRoSexec}(M)$
2:	Initialize the current configuration by default nodes;
3:	Initialize input set $I$ ;
4:	Initialize clock;
5:	while true do
6:	Read external input events into $I$ ;
7:	while $I$ is not empty <b>do</b>
8:	Execute instantaneous events;
9:	end while
10:	Execute timed actions ;
11:	Increase clock ;
12:	end while
13:	end procedure

# Transformation of SC into TROS



#### Annotation



#### Transformation



Algorithm 1 TRoSexec

- 1: **procedure** TROSEXEC(M)
- 2: Initialize the current configuration by default nodes;
- 3: Initialize input set I;
- 4: Initialize clock ;
- 5: while true do
- 6: Read external input events into I;
- 7: while I is not empty do
- 8: Execute instantaneous events ;
- 9: end while
- 10: Execute timed actions ;
- 11: Increase clock ;
- 12: end while
- 13: end procedure



#### ACSR

- A formal specification for real-time systems
- The actions of ACSR consists of two kinds of actions: timed actions and event actions.

#### Sematnics

ActT:-ActI:- $A: P \xrightarrow{A} P$ ActI:- $(a, n).P \xrightarrow{(a, n)} P$ Choice: $\frac{P \xrightarrow{\alpha} P_i, i = 1, 2}{P_1 + P_2 \xrightarrow{\alpha} P_i}$ Par: $\frac{P \xrightarrow{\alpha} P_i, i = 1, 2}{P_1 ||P_2 \xrightarrow{\alpha} P_i}$ **ParT:**  $\frac{P \xrightarrow{A_1} P', Q \xrightarrow{A_2} Q'}{P || Q \xrightarrow{A_1 \cup A_2} P' || Q'} \quad (\rho(A_1) \cap \rho(A_2)) = \emptyset$ **ParC:**  $\xrightarrow{P \xrightarrow{(l,m)}} P', Q \xrightarrow{(inv(l),n)} Q'$  $P||Q \xrightarrow{(\tau,m+n)} P'||Q'$ CloseT:  $\frac{P \xrightarrow{A_1} P'}{[P]_I \xrightarrow{A_1 \cup A_2} [P']_I} \quad (A_2 = \{(r,0) | r \in I - \rho(A_1)\})$ CloseI:  $\frac{P \xrightarrow{e} P'}{[P]_I \xrightarrow{e} [P']_I}$  ResT:  $\frac{P \xrightarrow{A} P'}{P \setminus F \xrightarrow{A} P' \setminus F}$  $\textbf{ResI:} \ \ \frac{P \xrightarrow{e} P'}{P \backslash F \xrightarrow{e} P' \backslash F} \quad (\gamma(e) \notin F)$  $\textbf{HideT:} \quad \frac{P \xrightarrow{A} P'}{P \setminus \backslash H \xrightarrow{A} P' \setminus \backslash H} \quad (A' = \{(r, n) \in A | r \notin H\})$ HideI:  $\frac{P \xrightarrow{e} P'}{P \setminus H \xrightarrow{e} P' \setminus H}$ 

The nonprioritized relation of ACSR

## **Preemption Relations**

there are three cases for  $\alpha < \beta$ :

1)  $\alpha$  and  $\beta$  are events with the same label and  $\beta$  has a higher priority;

2)  $\alpha$  and  $\beta$  are actions and  $\beta$  uses a subset of resources with the following two conditions: all resources in  $\beta$  have at least the same priority as in  $\beta$ and at least one at a higher priority than in  $\alpha$ , and every resources in  $\alpha$  that is not in  $\beta$  must have a zero priority;

3)  $\beta$  is a  $\tau$  event with a non-zero priority while  $\alpha$  is a timed action;

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# TRoS || ACSR

 it represents a composition of two behavioral systems in TRoS and ACSR respectively.





#### Semantics

- The behavior of TRoS || ACSR is defined in the same way defining ACSR,
- First, the non-priority relations of their composite behavior are defined,
- Then, the non-prioritized behaviors are prioritized based on preemption relations

# Synchronization Event for TRoS

#### $E ::= \epsilon | (e?, ve) \cdot E | (e!, ve) \cdot E$

# TRoS in TRoS || ACSR

- A TRoS is defined as a tuple (N, T, E, G, A), where
  - N is a set of nodes,
  - T is a set of transitions,
  - E is a set of primitive event expressions,
  - G is a set of guard expressions,
  - A is a set of action expressions.

# TRoS in TRoS || ACSR

- $I_{B} = E \cup A$ : Broadcasting event communicating within TRoS
- Is: Synchronization event between TRoS and ACSR

## A Behavior of TRoS

- A behavior of a system in TRoS is defined as a set of possible of runs[HN96]. A run is a series of statuses. A status is defined by the tuple s<sup>T</sup> = (C<sub>B</sub>,C<sub>T</sub>,clk,l), where
  - $C_B$  denotes a basic configuration,
  - $C_T$  denotes a timed configuration,
  - *clk* denotes the current time,
  - $I = I_B \cup I_S$ .

## A Run of TRoS

• A run of the system is a sequence  $c^T = s^T_0 s^T_1 \dots s^T_n$ , and we let  $S^T$  be the set of statuses.

# ACSR in TRoS || ACSR

- A system of ACSR is represented in labeled transition system  $T^A = (S^A, Act, \rightarrow \pi, s^A_0)$  [LPS07], where
  - $S^A$  is a set of states,
  - Act includes event and timed actions,
  - $\rightarrow_{\pi}$ : prioritized transitions,
  - s<sup>A</sup><sub>0</sub>: Initial parallel processes (A configuration of processes)

# TRoS || ACSR

- A system of TRoS  $\parallel$  ACSR is defined in a labeled transition system  $T = (S, A_{sync}, \rightarrow_{\sigma}, s_0)$ , where
  - $S = \{(s^T, s^A) \mid s^T \in S^T \text{ and } s^A \in S^A\},\$
  - $A_{sync} = I_S \cup \{e \mid inv(e) \in Act\} \cup \{\epsilon\},\$
  - $\rightarrow_{\sigma}$ : prioritized transition relation of TRoS || ACSR
  - s<sub>0</sub> : Initial configuration of TRoS || ACSR consisting of default state nodes and initial processes.

# TRoS || ACSR

- The event ε implies that no action takes place in ACSR when a step in TRoS is executed.
- A behavior of TRoS  $|| ACSR is defined by a sequence of <math>c = s_0\beta_1s_1...\beta_ns_n$ , such that  $si \in S$ ,  $\beta i \in Act \cup \{\epsilon\}$ , and  $(si,\beta i+1,si+1) \in \neg \sigma$ , for all  $0 \le i \le n$ .

# Non-prioritized Relation of TRoS || ACSR

$$\begin{split} \overline{s_i^T = (\mathcal{C}_B, \mathcal{C}_T, clk, I), s_i^A \in S^A} \\ \mathbf{TauAct:} & \frac{s_i^A \xrightarrow{(\tau,m)} \pi s_{i+1}^A}{(s_i^T, s_i^A) \xrightarrow{(\tau,m)} (s_i^T, s_{i+1}^A)} \\ \mathbf{ACSREventAct:} & \frac{s_i^A \xrightarrow{e} \pi s_{i+1}^A}{(s_i^T, s_i^A) \xrightarrow{e} (s_i^T, s_{i+1}^A)}, \text{ where } inv(e) \notin I_S \\ \mathbf{TRoSEventAct:} & \frac{s_i^T \rightarrow_\gamma s_{i+1}^T, s_{i+1}^T = (\mathcal{C}'_B, \mathcal{C}_T, clk, I')}{(s_i^T, s_i^A) \xrightarrow{e} (s_i^T, s_i^A)} \\ & , \text{ where } I_S = \emptyset \end{split}$$

EventComm:

$$\begin{array}{c} \underbrace{s_i^T \rightarrow_{\gamma} s_{i+1}^T, s_i^A \xrightarrow{(a!,m)}_{\pi} s_{i+1}^A, s_{i+1}^T = (\mathcal{C}'_B, \mathcal{C}_T, clk, I')}_{(s_i^T, s_i^A) \xrightarrow{(\tau, n+m)}} (s_{i+1}^T, s_{i+1}^A) \\ & , \text{ where } (a?, n) \in I_S \text{ and } (a?, n) \notin I' \end{array}$$

TimedAct:

$$\frac{s_i^T \xrightarrow{A_1} \gamma \ s_{i+1}^T, s_i^A \xrightarrow{A_2} \pi \ s_{i+1}^A, s_{i+1}^T = (\mathcal{C}_B, \mathcal{C}'_T, clk + 1, I')}{(s_i^T, s_i^A) \xrightarrow{A_1 \cup A_2} (s_{i+1}^T, s_{i+1}^A)},$$
where  $\rho(A_1) \cap \rho(A_1) = \emptyset$ 

# A Status of TRoS || ACSR

 $s_i^T = (\mathcal{C}_B, \mathcal{C}_T, clk, I), \ s_i^A \in S^A$ 

### Rule : TauAct



 $\xrightarrow{(\tau,m)}_{\pi} s^A_{i+1}$  $s^A_i$  $(s_i^T, s_i^A) \xrightarrow{(\tau, m)} (s_i^T, s_{i+1}^A)$ 

### Rule : TauAct



 $\xrightarrow{(\tau,m)}_{\pi} s^A_{i+1}$  $s^A_i$  $(s_i^T, s_i^A) \xrightarrow{(\tau, m)} (s_i^T, s_{i+1}^A)$
#### Rule : ACSREventAct



$$\frac{s_i^A \xrightarrow{e}_{\pi} s_{i+1}^A}{(s_i^T, s_i^A) \xrightarrow{e} (s_i^T, s_{i+1}^A)}, \text{ where } inv(e) \notin I_S$$

#### Rule : TRoSEventAct



$$\begin{split} \frac{s_i^T \rightarrow_{\gamma} s_{i+1}^T, s_{i+1}^T = (\mathcal{C}'_B, \mathcal{C}_T, clk, I')}{(s_i^T, s_i^A) \xrightarrow{\epsilon} (s_{i+1}^T, s_i^A)} \\ , \text{ where } I_S = \end{split}$$

Ø

#### Rule : TimedAction



#### Rule : EventComm



$$\begin{array}{c} \underbrace{s_i^T \rightarrow_{\gamma} s_{i+1}^T, s_i^A \xrightarrow{(a!,m)}_{\pi} s_{i+1}^A, s_{i+1}^T = (\mathcal{C}'_B, \mathcal{C}_T, clk, I')}_{(s_i^T, s_i^A) \xrightarrow{(\tau,n+m)}} (s_{i+1}^T, s_{i+1}^A) \\ & \quad , \text{ where } (a?,n) \in I_S \text{ and } (a?,n) \notin I' \end{array}$$

#### **Preemption Relation**

- Definition (Preemption Relation for TRoS || ACSR) Given two actions α and β, we say that β preempts α, denoted by (α < β), if one of the following cases hold:</li>
  - I. Both  $\alpha$  and  $\beta$  are events in  $D_{\epsilon}$ , where  $\alpha = (a,p), \beta = (a,p')$ , and p < p'
  - 2. Both  $\alpha$  and  $\beta$  are actions in  $D_R$ , where  $(\rho(\beta) \subseteq \rho(\alpha)) \land$   $(\forall (r,p) \in \alpha(((r,p') \in \beta \Longrightarrow p \le p') \land ((r,p')/=\beta \Longrightarrow p = 0))) \land$  $(\exists (r,p') \in \beta \exists (r,p) \in \alpha.p < p')$
  - 3.  $\alpha \in D_R$  and  $\beta \in D_E$ , with  $\beta = (\tau, p)$  and p > 0
  - 4.  $\alpha = \epsilon$  and  $\beta \in D_{\epsilon}$ , with  $\beta = (\tau, p)$  and p > 0
  - **5.**  $\alpha \in D_R$  and  $\beta = \epsilon$ .

#### **Prioritized Transition System**

Definition) The labeled transition " $\rightarrow \sigma$ " is defined as follows:  $s \xrightarrow{\alpha} \sigma s'$  if and only if 1)  $s \rightarrow s'$  is an unprioritized transition and 2) there is no unprioritized transition system s  $\xrightarrow{\beta}$  s'' such that  $\alpha < \beta$ *B*.

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## Specifying RTOS Services

- Scheduling
- Synchronization
- Communication
- Time management

#### Semaphore in ACSR

### Message Queue

### Time Manager

#### Example of TRoS || ACSR































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 the semantics of a composition of TRoS || ACSR representing application software and RTOS respectively

#### • TRoS

- extends Statecharts in terms of time and resource constraints by annotation methods.
- presents a way to gain a timed and resource-constrained behavioral model from Statecharts in easy way.

#### • ACSR

useful to capture RTOS in easy way

- provides explicit notion of timely prioritized use of resource
- able to verified with FM verification tools

- TRoS || ACSR
  - defines a composition of two different systems representing application software and platform software, i.e., RTOS.

- This work contributes to
  - independently designing application software and platform software with their appropriate formal specification languages,
  - analyzing their composite behaviors based on our behavioral semantics of TRoS || ACSR.

#### References

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# Thanks



### Avionics Systems based on ARINC 653


## Position Indicator in TRoS



## ARINC 653 Semaphore in ACSR

BUFFER\_SERVICE = SERVICE\_BUFF1 ;

SERVICE_BUFF1 =	<pre>= SERVICE_BUFF_buff1    STAT_BUFF_buff1 ;</pre>
STAT_BUFF_buff1 =	= {}:STAT_BUFF_buff1
-	+ (Get_buff1_id,1)
	.(('get_buff1_id_NO_ERROR,1)
	.STAT_BUFF_buff1
	+ (buff1_name_not_identified,1)
	.('get_buff1_id_INVALID_CONFIG,1)
	.STAT_BUFF_buff1)
-	+ (Get_buff1_status,1)
	.(('get_buff1_status_NO_ERROR,1)
	.STAT_BUFF_buff1
	+ (buff1_ID_not_identified,1)
	.('get_buff1_status_INVALID_CONFIG,1)
	.STAT_BUFF_buff1) ;
SERVICE_BUFF_buff	f1 = CREATEBUFF_buff1 ;
CREATEBUFF_buff1	= {}:CREATEBUFF_buff1 + (Crt_buff1,1).CrtBUFFRtnCdbuff1;
CrtBUFFRtnCdbuff	<pre>l= ('crtbuff1_NU_ERROR,1).BUFFinSVC_buff1</pre>
	+ (no enough buff space.1)

## Verification

C:#Documents and Settings#Administrator#My Documents#My Works#Chi\_Paper Work#ROD #TROS Series 2#TRoS..ACSR#APEX ACSR model>uxp ARINC653basedTasks.acsr :-> SYS! (x> <1> --(tau,(7+1>)@ExSTART\_PI--> (x> [(x>] :-O show stats State machine contains 153 reachable states <0 deadlocked>, 153 edges. Edges represent 120 timed transitions, 27 internal actions, and 6 external untimed actions. LTS is non-Zeno. 6 non-deadlocked states are capable of stopping the clock.

Time to compute LTS: 0 seconds user time; 0 seconds system time. [(x)] :-0 \_