

"IN THE PAST VEHICLE SAFETY HAS BEEN CONSTRUCTED; IN THE FUTURE IT IS GOING TO BE IMPLEMENTED IN SOFTWARE." Dr. U. Widmann, AUDI AG, Head of Vehicle Safety

- Automobile turns into time and safety sensitive systems
- Dealing with safety requirements is major challenge
 - ⇒ Dependability Analyses in Design and Verification Phases



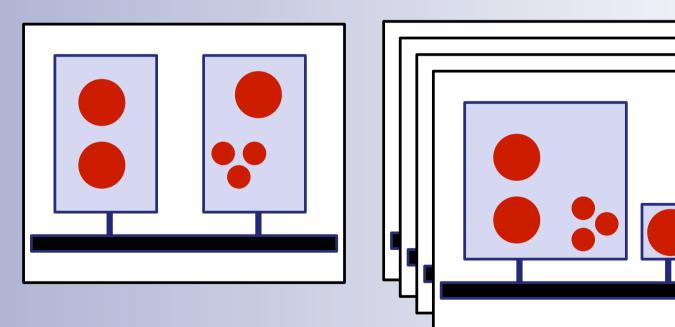
INUL Fredrich Alexander mgotstatt Institute der Friedrich Alexander

> .<mark>udi</mark> orsprung durch Technik

21 novembr 10 Fault Tree Synthesis from UML Models for Reliability Analysis at Early Design Stages



- Rapidly changing requirements and design concepts
- Effects on dependability attributes unknown
 - Analyses time consuming
 - Expert Knowledge required
- Analyses often at later development stages



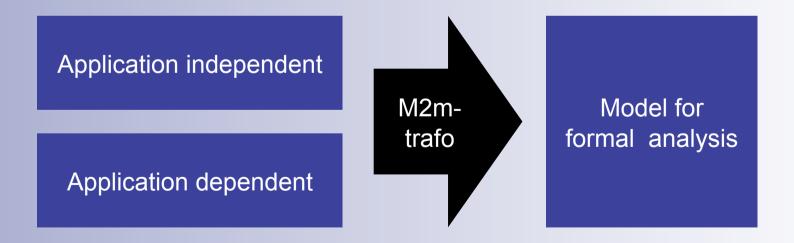
INITE Frediction Alexand

<mark>Audi</mark> Vorsprung durch Technik

Fault Tree Synthesis from UML Models for Reliability Analysis at Early Design Stages



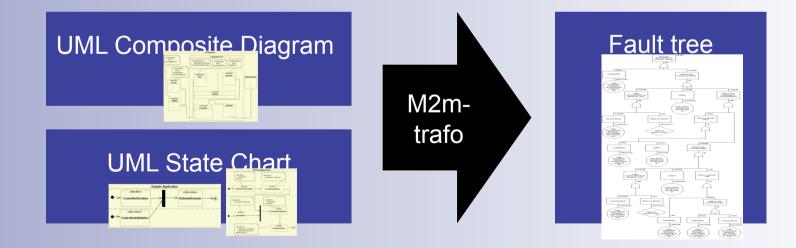
- Model-to-model transformation
- Hide complexity of formal method
- Chose modeling approach to increase reusability of the models
 - Small changes in system architecture require small changes in model
 - Separate application dependent and application independent system views

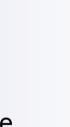






- Model HW/SW-Architecture in UML Composite Diagrams
- Model Applications in UML State Charts
- Run synthesis algorithm to transform the model into a fault tree representation for further analyses



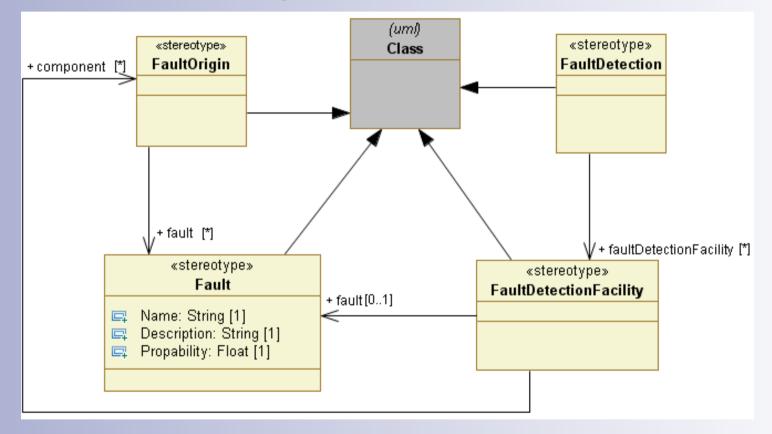


Z



Modeling the HW/SW-Architecture

• UML Composite Diagrams



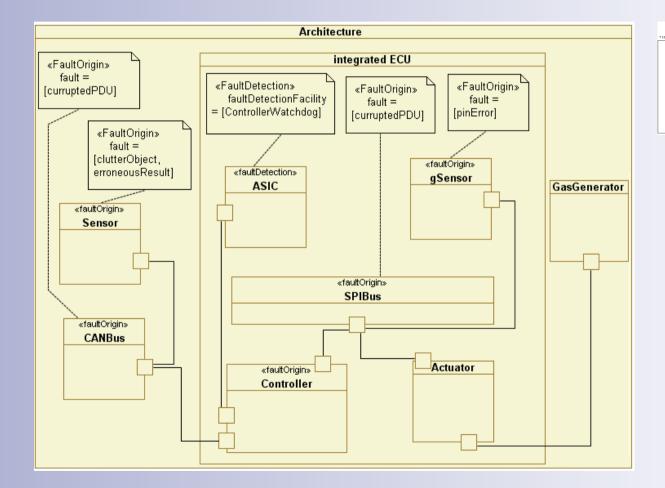


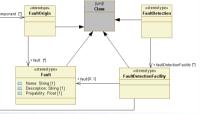


Modeling the HW/SW-Architecture



• UML Composite Diagrams

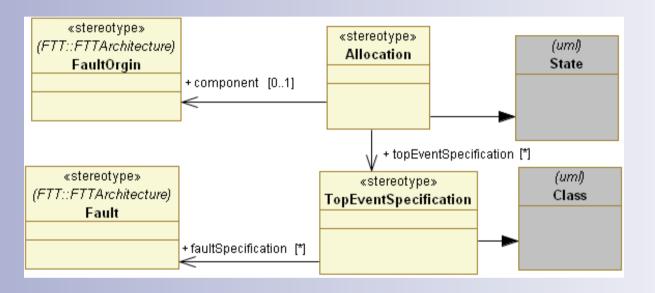


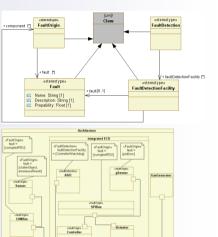




Modeling the Applications

UML State Charts



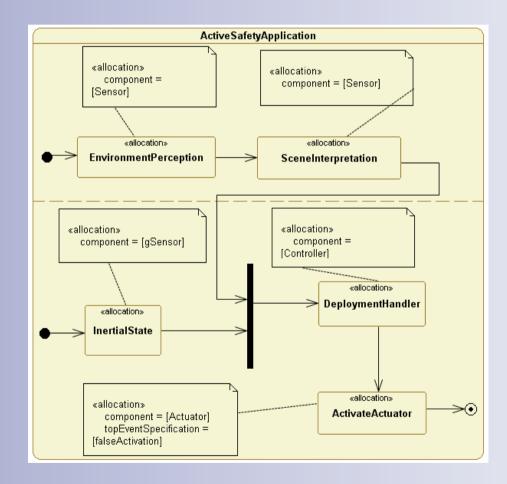


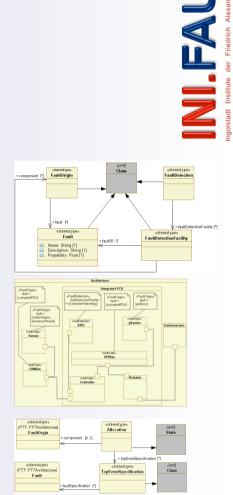
INITE OF Friedrich Alexand Universität Erlangen-Nürmberg



Modeling the Applications

• UML State Charts

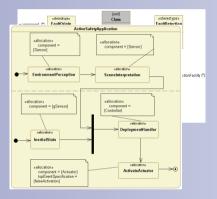


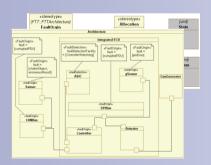




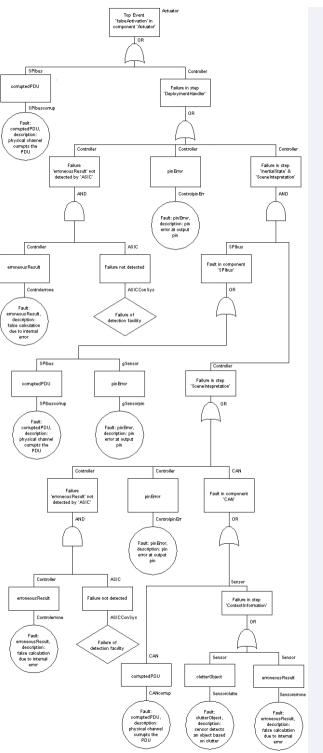
The Transformation Ste

Model-to-model transformation (E)

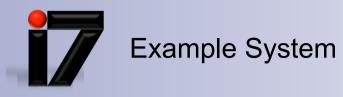




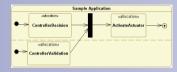


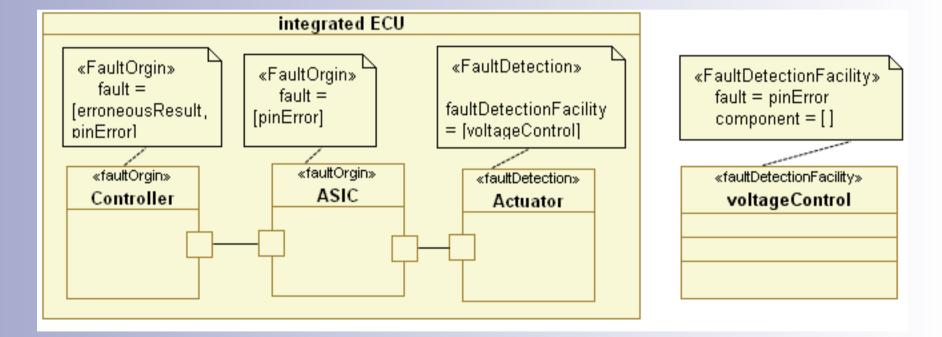


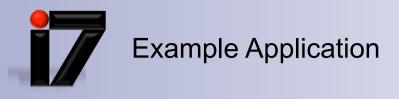


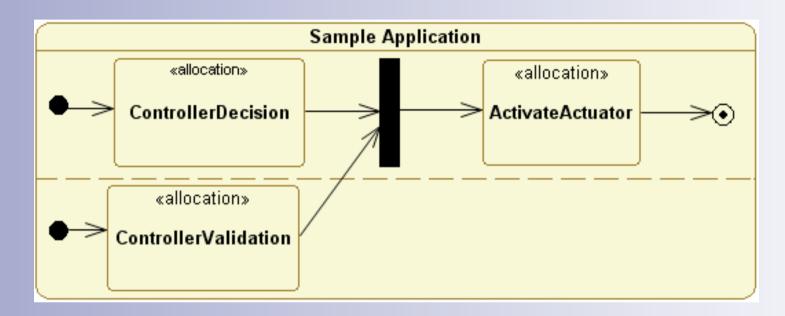






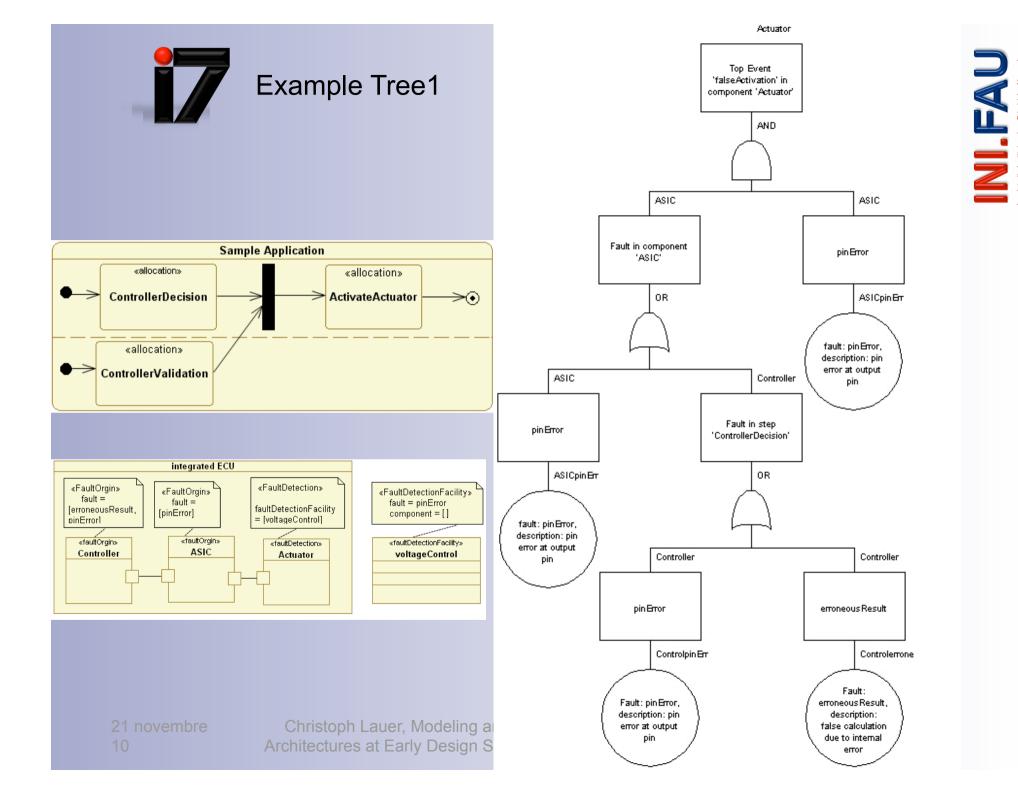


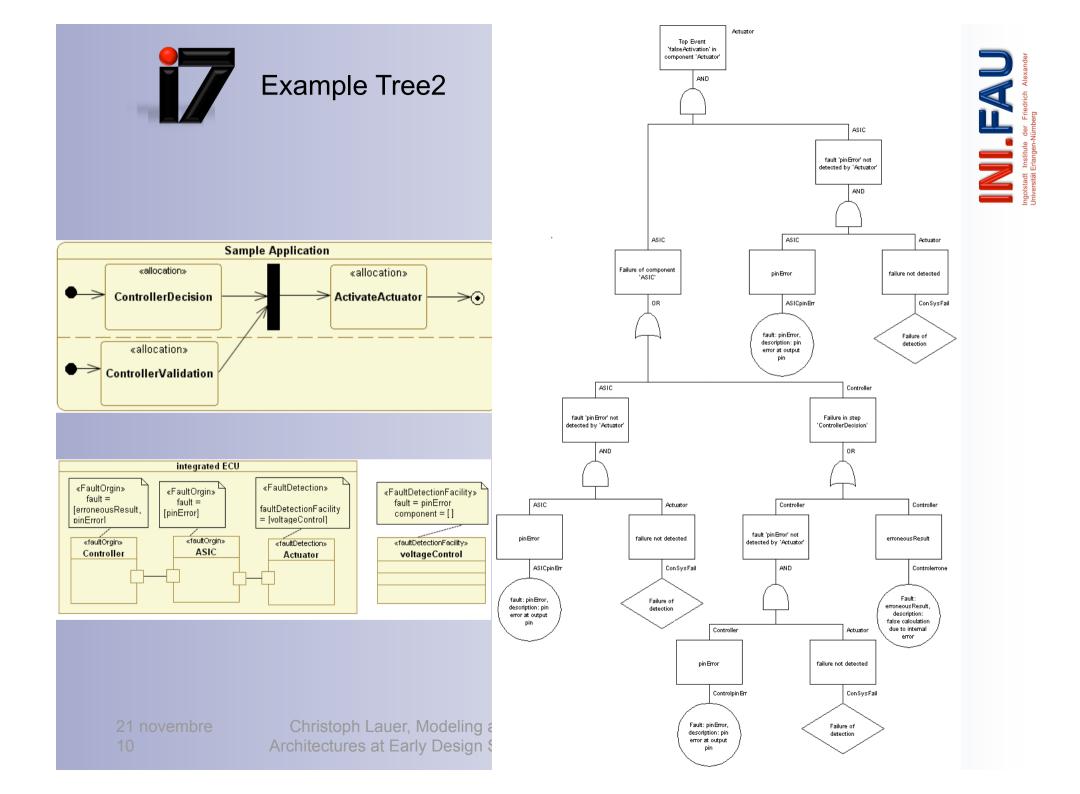






integrated ECU	
of autogins Auto of autogins (arrange autogins) of autogins (arrange autogins) of autogins) of autogins (arrange autogins) frage autogins) frage autogeneration (arrange autogins) of autogins) of autogins) frage autogeneration (arrange autogeneration) of autogeneration frage autogeneration (arrange autogeneration) of autogeneration frage autogeneration (arrange autogeneration) of autogeneration frage autogeneration) of autogeneratio	eFaultDetectionFacilitys fault = pinterror component = [] enadDetectorFacilys voltageControl







- Automatic and model based FTA "interesting" for early design stages
- Modeling in UML from two different perspectives
 - Application independent
 - Application dependent
- Low remodeling effort suggests reusability
 - No proof given, though
- Transformation leads to plausible fault trees
 - Optimization possible
- Lots of research potential

