

SymTA/S by Symtavision

State-of-the-art in standards and tools for schedulability analysis

ArtistDesign Workshop on Real-Time System Models for Schedulability Analysis

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Solutions for Complex

Real-Time Systems



Symtavision – Who we are

Timing analysis experts for embedded real-time systems

Company

- Founded 2005
- 20 employees
- Based in Braunschweig, Germany
 - Munich branch
 - Distributors in France, Italy, Japan, China, Korea
- Customers: Bosch, Daimler, VW, GM, Toyota …

De-facto Standard Solutions

- ► Timing analysis tools: SymTA/STM, TraceAnalyzerTM
- Integration with best-in-class partners
- Engineering and methodology services



Dr. Jersak CEO



Dr. Richter



W. Ries CSO



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Our Customers

OEMs & Suppliers

- Automotive
- Aerospace

Focus

- Powertrain
- Chassis & Safety
- Driving assistance
- Network and EE-architecture
- Domain integration



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SymTA/S Demo



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SymTA/S – advantages

- Standard scheduler combination, for example:
 - AUTOSAR: SPP + SPnP
 - FlexRay: TDMA + SPP
 - ARINC653: TDMA + SPP (hierarchical)
 - \rightarrow Combination of all with End-to-End analysis
- Analysis performance
 - Scales and usable for large industrial systems
- Usability
 - Domain specific models and terminology (e.g. AUTOSAR)
 - Import/Export interfaces
 - Scriptable



Daimler Example (2/2)



Applying Timing Analysis to Vehicle Networking

Handling large Vehicle Systems in SymTA/S

A SymTA/S-System based on AUTOSAR-XML import consists of all ECUvariants, busses, PDUs and signals defined. It has to be configured for various analyses.

According to our experience the current release of SymTA/S

- can handle a complete vehicle network system on a 64-Bit Workstation
- lacks automation mechanisms for efficient configuration
- benefits little of today's computing power (single thread computation)



limited by automation capability, computation efficiency and available memory.

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Further concepts

- Sensivity analysis
- Distribution analysis
- Experiment Framework
 - Data consistency
 - Communication overhead analysis (multicore)





Thank You !

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