



**EUROPEAN COMMISSION**  
Information Society and Media Directorate-General  
Embedded Systems and Control Units >  
G3

Brussels, **03 MAI 2012**  
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Olivier Guerard  
Floralis – UJF Filiale  
Allée de Bethléem 6  
38610 Gières  
FRANCE

**Subject: Result of the final review of your FP7/ICT project 214373 Artist Design**

Dear Mr Guerard,

Following the final review of your project held in **Dresden (DE)** on **16/03/2012** and based on the attached review report of the experts, the Commission considers the project as successfully completed pending the implementation of the following recommendations:

- The activities that are demonstrated in terms of joint works would benefit from having a joint future research agenda. ARTISDESIGN “branding” has been limited during the project and the consortium is encouraged to develop a promotion strategy for its future research vision.
- The consortium should reflect how to ensure sustainable support for documentation and training for tools developed in the project.
- The final publishable project report should reflect on a structure of a consolidated research agenda and cover the above aspects.

The Commission approval of deliverables is set out in the enclosed Annex 1.

Please note that the assessment of the use of the resources made by the experts in point 3 of the report does not imply the acceptance of the corresponding costs by the Commission.

The eligibility of the costs claimed will be assessed by the Commission on the basis of the financial statements submitted by each beneficiary. You will be soon informed about the amount of the final payment of the financial contribution of the European Union according to Article II.5.8 of the grant agreement.

All costs declared by a beneficiary can be subject to a financial audit pursuant to Article II.22 of the grant agreement.

According to Article II.23.8 of the grant agreement, you may make observations on the result of the review of your project within one month of reception of this letter.

We wish you and the whole consortium all the success for your future endeavours.

Yours sincerely,

A handwritten signature in black ink, appearing to read 'R. Riemenschneider', with a large, stylized flourish at the end.

Rolf Riemenschneider

Project Officer

## ANNEX 1: STATE OF PROJECT DELIVERABLES BY WP

Del. No.	Deliverable name	Comments	Status	File
<b>WP0: Joint Programme of Management Activities (JPMA)</b>				
D-0.1-Y4	Project Report		<b>Accepted</b>	D1 – received by the commission
D-0.1-Y4	Project Activity Report		<b>Accepted</b>	D2-0-2a-Y4_ExecSummary+Overview.pdf  D2-0-2b-Y4_Modelling_and_Validation.pdf  D2-0-2c-Y4_SW_Synthesis_Code_Generation_and_Timing_Analysis.pdf  D2-0-2d-Y4_Operating_Systems_and_Networks.pdf  D2-0-2e-Y4-Hardware_Platforms_and_MPSoC_Design.pdf
<b>WP1: Joint Programme of Integration Activities (JPIA)</b>				
D-1.0-Y4	Integration Activities Report		<b>Accepted</b>	D3-1-0-Y4_JPIA_Integration_Activities_Report.pdf

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**WP2: Joint Programme of Activities for Spreading Excellence (JPASE)**

D-2.0-Y4	Spreading Excellence Report		<b>Accepted</b>	D4-2-0-Y4_Spreading_Excellence.pdf
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**WP3: Thematic Cluster: Modeling and Validation (JPRA)**

D-3.1-Y4	Modelling Report		<b>Accepted</b>	D5-3-1-Y4_Modelling.pdf
D-3.2-Y4	Validation Report		<b>Accepted</b>	D6-3-2-Y4_Validation.pdf

**WP4: Thematic Cluster: Software Synthesis, Code Generation and Timing Analysis (JPRA)**

D-4.1-Y4	Software Synthesis, Code Generation		<b>Accepted</b>	D7-4.1-Y4_Software_Synthesis_Code_Generation.pdf
D-4.2-Y4	Timing Analysis		<b>Accepted</b>	D8-4-2-Y4_Timing_Analysis.pdf

**WP5: Thematic Cluster: Operating Systems and Networks (JPRA)**

D-5.1-Y4	Resource-Aware Operating Systems		<b>Accepted</b>	D9-5-1-Y4_Resource-aware_Operating_Systems .pdf
D-5.2-Y4	Scheduling and Resource Management		<b>Accepted</b>	D10-5-2-Y4_Scheduling_and_Resource_Management.pdf
D-5.3-Y4	Embedded Real-Time Networking		<b>Accepted</b>	D11-5-3-Y4_Embedded_Real_Time_Networking.pdf

**WP6: Thematic Cluster: Hardware Platforms and MPSoC Design**

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D-6.1-Y4	Platform and MPSoC Design		<b>Accepted</b>	D12-6-1-Y4_Platform_and_MPSoC_Design.pdf
D-6.2-Y4	Platform and MPSoC Analysis		<b>Accepted</b>	D13-6-2-Y4_Platform_and_MPSoC_Analysis.pdf

**WP7: Transversal Integration (JPRA)**

D-7.1-Y4	Design for Adaptivity		<b>Accepted</b>	D14-7-1-Y4_Design_for_Adaptivity.pdf
D-7.2-Y4	Design for Predictability		<b>Accepted</b>	D15-7-2-Y4_Predictability.pdf
D-7.3-Y4	Industrial Integration		<b>Accepted</b>	D16-7-3-Y4_Integration_Driven_by_Industrial_Applications.pdf

## LIST OF PO AND REVIEWERS FOR THIS REVIEW

Name	Organisation	Email
Rolf Riemenschneider	EC	rolf.riemenschneider@ec.europa.eu
Janos Sztipanovits	Vanderbilt	janos.sztipanovits@vanderbilt.edu
Martin Timmerman	Dedicated Systems Experts	m.timmerman@dedicated-systems.info

### AGENDA (AS EXECUTED)

March 16 2012 – Dresden Germany

Time	Presentation	Speakers
9:00	Introduction by EC	Rolf Riemenschneider
9:05	<b>Overview</b> Scientific Management Long-term Objectives and Status NoE Principles of Construction Integration of the area Building Excellence <b>File: 1_1_Sifakis_ScientificManagement.pdf</b>	Joseph Sifakis (UJF/VERIMAG)  Bruno Bouyssounouse (UJF/VERIMAG)
9:20	Modeling and Validation Cluster Achievements and Perspectives Overall Aims and Achievements (Integration, Building Excellence) Overview of Scientific Highlights in Y4 Discussion <b>File: 2_2 Larsen Graf - Review.pdf</b>	Kim Larsen (Aalborg)  Susanne Graf (UJF/Verimag)
09:48	SW Synthesis, Code Generation and Timing Analysis Cluster	Peter Marwedel (Dortmund)

	<p>Achievements and Perspectives - SW Synthesis, Code Generation</p> <p>Overall Aims and Achievements (Integration, Building Excellence)</p> <p>Overview of Scientific Highlights in Y4</p> <p>Discussion</p> <p><b>File: 3 Marwedel Lisper - Review.pdf</b></p>	<p>Björn Lisper (Mälardalen)</p>
10:24	Break	
11:05	<p>Operating Systems and Networks Cluster</p> <p>Achievements and Perspectives</p> <p>Overall Aims and Achievements (Integration, Building Excellence)</p> <p>Overview of Scientific Highlights in Y4</p> <p>Discussion</p> <p><b>Files: 4a Buttazzo - Review.pdf, 4b Burns - Review.pdf</b></p>	<p>Giorgio Buttazzo (Scuola Sant'Anna - Pisa)</p> <p>Alan Burns (York)</p> <p>Luis Almeida (U.Porto)</p>
11:26	<p>Hardware Platforms and MPSoC Design Cluster</p> <p>Achievements and Perspectives</p> <p>Overall Aims and Achievements (Integration, Building Excellence)</p> <p>Overview of Scientific Highlights in Y4</p> <p>Discussion</p> <p><b>File: 5 Madsen - Review.pdf</b></p>	<p>Jan Madsen (DTU) - presenter</p> <p>Luca Benini (Bologna) -</p>
12:00	Lunch	
13:00	<p>Integration Driven by Industrial Applications</p> <p>Achievements and Perspectives</p> <p>Overall Aims and Achievements (Integration, Building Excellence)</p> <p>Overview of Scientific Highlights in Y4</p> <p>Discussion</p> <p><b>File: 8 Sangiovanni - Review.pdf</b></p>	<p>Alberto Sangiovanni (TRENTO)</p>
13:45	<p>Design for Predictability and Performance</p> <p>Achievements and Perspectives</p> <p>Overall Aims and Achievements (Integration, Building Excellence)</p> <p>Overview of Scientific Highlights in Y4</p>	<p>Bengt Jonsson(Uppsala)</p>

	<p>Discussion</p> <p><b>File: 7 Jonsson - Review.pdf</b></p>	
14:10	Break	
14:25	<p>Design for Adaptivity</p> <p>Achievements and Perspectives</p> <p>Overall Aims and Achievements (Integration, Building Excellence)</p> <p>Overview of Scientific Highlights in Y4</p> <p>Discussion</p> <p><b>File: 6 Arzen - Review.pdf</b></p>	Karl-Erik Årzen (Lund)
15:00	<p>Spreading Excellence</p> <p>Achievements and Perspectives</p> <p>Vision: Long-term impact</p> <p>ArtistDesign Web Portal</p> <p>Year 4 Events</p> <p>Discussion</p> <p><b>Files: 9 Bouyssounouse - Review.pdf</b></p>	Bruno Bouyssounouse (UJF/VERIMAG)
15:10	<p>Administration, Budget and Efforts</p> <p>Principles / procedures</p> <p>Main efforts in Y4</p> <p><b>File: 10_Bouyssounouse_Administration+Budget_Management.pdf</b></p>	Bruno Bouyssounouse (UJF/VERIMAG)
15:15	<b>Reviewer's meeting</b>	
16:00	<b>Conclusion and Feedback</b>	
16:30	<b>End</b>	



**Annex 2**  
**Review report**