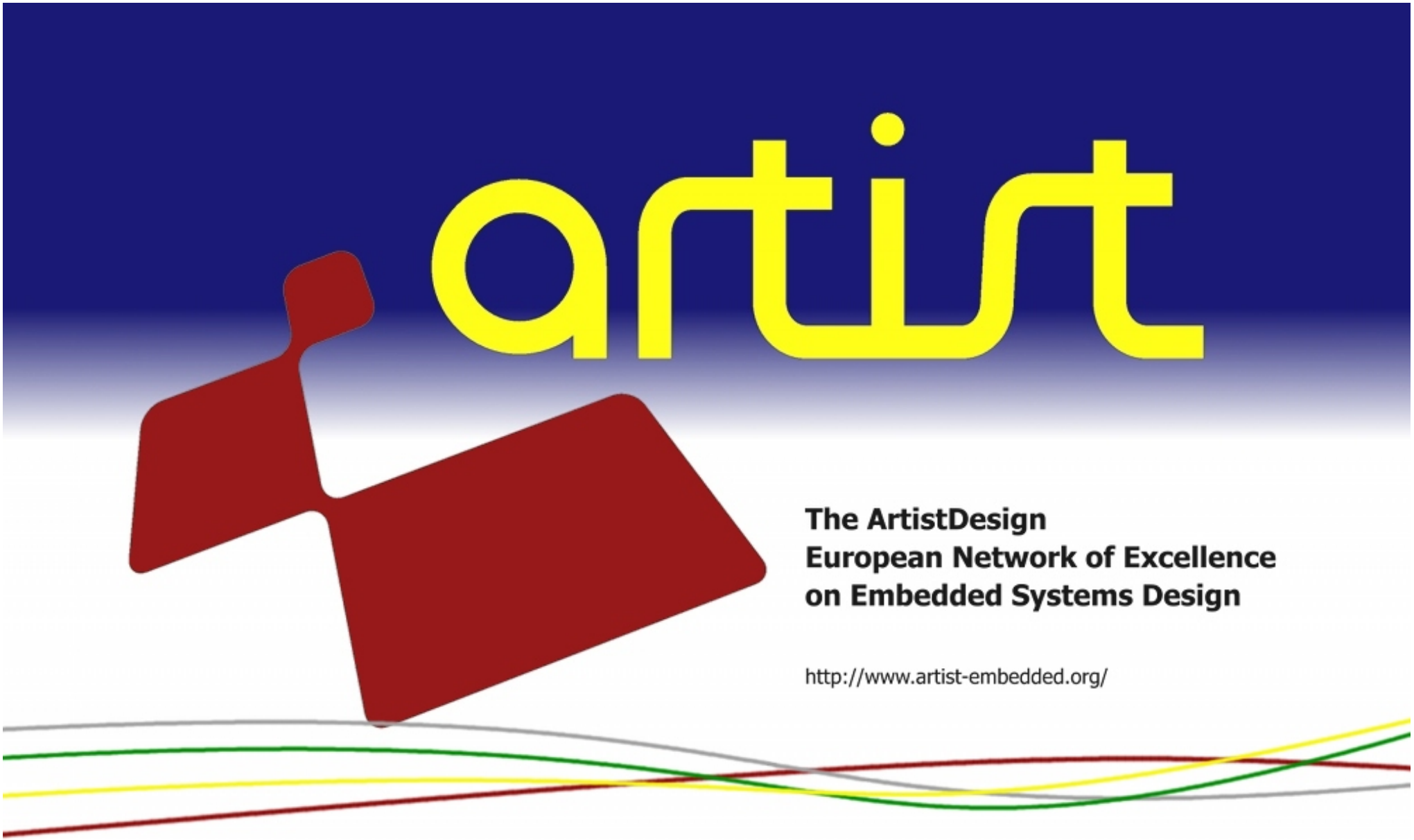


Showcase of the Main Results

DATE Conference, March 15th, 2012

SEVENTH FRAMEWORK PROGRAMME

ArtistDesign European NoE: Showcase of the Main Results

DATE Conference, March 15th, 2012

Achievements and Perspectives :

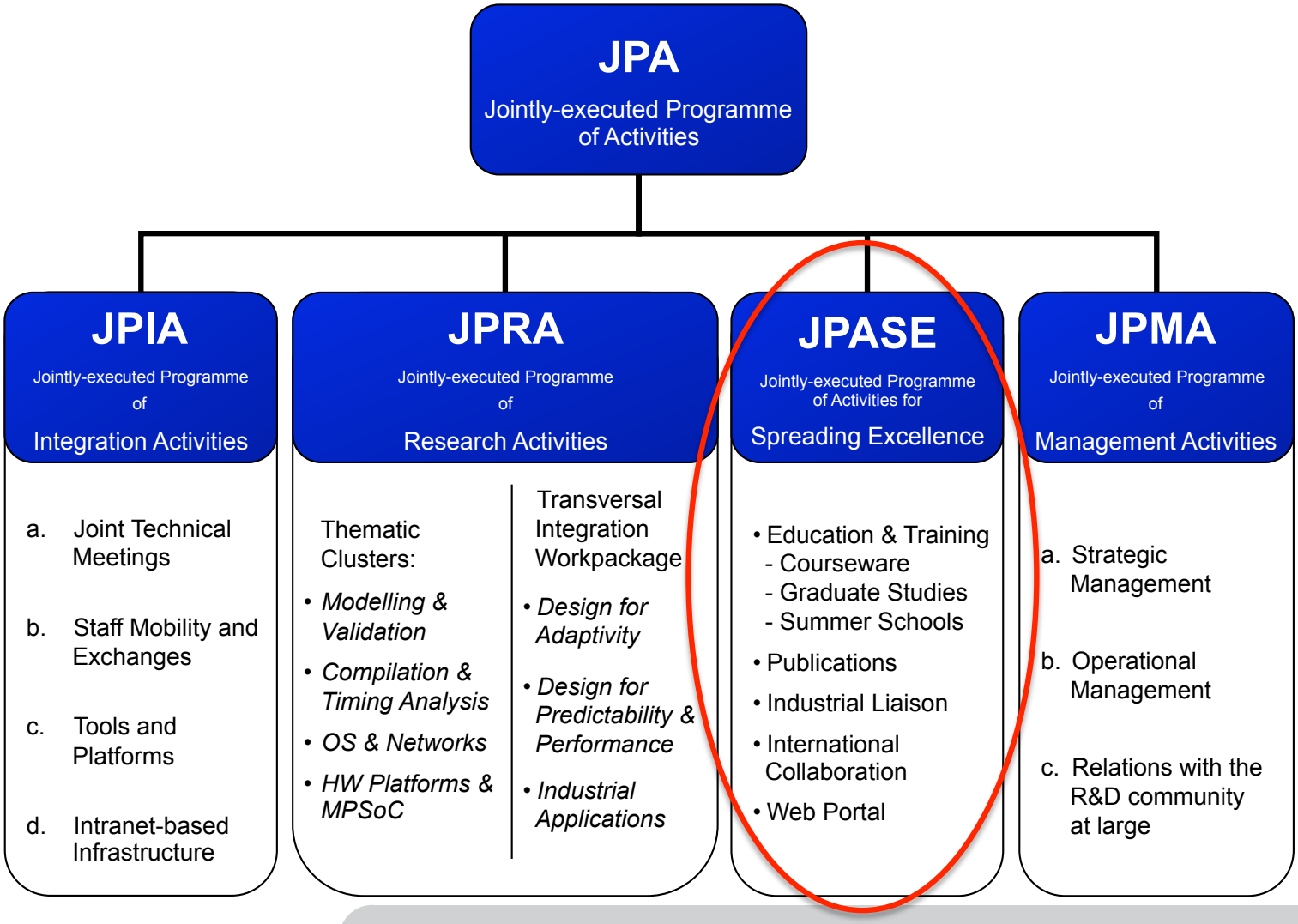
Events and Dissemination

leader : Bruno Bouyssounouse

UJF/Verimag

Joint Programme of Activities

ArtistDesign acts as a Virtual Centre of Excellence, composed of a set of virtual teams, called clusters. Each cluster gathers together selected teams from partners, to create the critical mass and expertise in one of the essential topics for embedded systems design.



Overall Vision and Strategy

Overall objective is the emergence of Embedded Systems Design as a scientific discipline. This objective is pursued within the international scientific and industrial community.

This is implemented in 3 levels:

- **International Collaboration**
  - High-level meetings (NSF/IST),
  - International Schools (eg: European school, China school, SouthAmerican school),
  - support for selected conferences (eg: Embedded Systems Week)
- **European level**
  - Direct organisation of top workshops and schools
  - Support for existing workshops, Artist and other schools
  - Industrial Liaison (ARTEMIS, triggering projects)
- **Affiliated Partners**
  - Direct involvement in the workprogramme (technical meetings).

Web Portal Newsletter Mailing List

Main Highlights in Y4

ARTIST Summer School in Europe 2011 - 7th edition

- high quality technical programme, excellent feedback from participants
- 73 paying participants and 14 invited speakers.

International ARTIST Summer School in China 2011 – 6th edition

Graduate Schools:

- **ARTIST Quantitative Model Checking Winter School 2012**
  February 27th - March 1st 2012 Copenhagen, Denmark
- **Time-Predictable and Composable Architectures for Dependable Embedded Systems**
  October 9th, 2011 Taipei, Taiwan
- **ARTIST Summer School on ICT for Future Energy Systems**
  July 25-29, 2011 Povo, Trento, Italy
- **ARTIST Graduate School on RT Kernels for Microcontrollers**
  June 13-17, 2011 Pisa, Italy

ARTIST Workshops

NERES, ACES^{MB}, WSS'11, WESE, TP&CADES, JTRES, FORMATS, IRTAW-15, WCET, RTN, Map2MPSoC, APRES, RED, PPES, ArtistDesign Workshop on Real-Time System Models for Schedulability Analysis, Synchronous Programming of Device Drivers for Global Resource Control in Embedded Operating Systems

ARTIST web portal

Recurring Events

red = recurring over several years

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ARTIST web portal

International Collaboration

blue = international collaboration

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ARTIST web portal

Education

green = education

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ARTIST web portal

Education: WESE Workshops

ArtistDesign:

- ***WESE'11: WS on Embedded Systems Education*** *October 13th, 2011 Taipei (Taiwan)*
- ***WESE'10: WS on Embedded Systems Education*** *October 28th, 2010 Scottsdale, USA (in ESWEEK)*
- ***WESE'09: WS on Embedded Systems Education*** *October 15th, 2009 Grenoble, France (in ESWEEK)*
- ***WESE'08: WS on Embedded Systems Education*** *October 23rd, 2008 Atlanta, Georgia - USA (in ESWEEK)*

Artist2:

- ***WESE'07: WS on Embedded Systems Education*** *October 4-5, 2007 Salzburg, Austria (within ES Week)*
- ***WESE'06 - Embedded Systems Education*** *October 26th, 2006 Seoul, Korea*
- ***WESE'05 - WS on Embedded Systems Education*** *September 22nd, 2005 Jersey City – USA*

- **ACM - Special Issue on Education**

and back in Artist FP5:

- ***Artist International Collaboration Days 2003 - Education*** *October 11th 2003 – Philadelphia*
- **Artist FP5 Guidelines for a Graduate Curriculum on Embedded (publication)**

Workshops organized and funded in Y4 (1/5)

- **NERES 2011**
  November 10-11, 2011 *University of Porto, Portugal*
  Along the past decades, several network communication protocols have been developed with new capabilities, from an ever increasing throughput and support for traffic classes (including guaranteed latency and jitter), to different topologies, integration of heterogeneous segments, extensive use of wireless technologies, openness to dynamic arrival and departures of nodes, openness to larger networks (such as the Internet), etc. If, on one hand, many problems have been solved, with a significant number of successful real-time embedded applications that rely on networking services, on the other hand new problems appeared, or some old problems persist, that still require adequate solutions for harmonization with real-time constraints, e.g., energy-efficient communication (particularly in WSN), networks for nodes with scarce resources, scalability issues in large sensor systems, networking support to middleware and to Quality-of-Service (QoS) adaptation and graceful degradation, support to higher software integration and transition to wireless communication everywhere.

- **ACES^{MB} 2011**
  October 18th, 2011 *Wellington (New-Zealand)* *(during MoDELS 2011)*
  The objective of this workshop is to bring together researchers and practitioners interested in model-based software engineering for real-time embedded systems. We are seeking contributions relating to this subject at different levels, from modelling languages and semantics to concrete application experiments, from model analysis techniques to model-based implementation and deployment. Given the criticality of the application domain, we particularly focus on model-based approaches yielding efficient and provably correct designs. Concerning models and languages, we welcome contributions presenting novel modelling approaches as well as contributions evaluating existing ones.

Workshops organized and funded in Y4 (2/5)

- **WSS'11**
  October 14th, 2011 *Taipei (Taiwan), within ESWeek 2011*
  An increasing amount of software is not written manually any more. Rather, software is synthesized from abstract models of the required functionality. As a result, the effort of generating software is reduced and software verification typically becomes easier.
  Software synthesis has been implemented in various disperse communities. The workshop aims at bringing the software generation and software synthesis communities together and at identifying research problems which should be addressed by the scientific community.

- **WESE 2011**
  October 13th, 2011 *Taipei (Taiwan), within ESWeek 2011*
  As embedded system designs grow more complex and the time to market diminishes, quality embedded systems education becomes more and more important. This fifth workshop on the subject aims to bring researchers, educators, and industrial representatives together to assess needs and share design, research, and experiences in embedded systems education.

- **JTRES – 2011**
  September 26-28, 2011 *Kings Manor, York, England*
  Interest in real-time Java in both the research community and industry has recently increased significantly, because of its challenges and its potential impact on the development of embedded and real-time applications. The goal of the proposed workshop is to gather researchers working on real-time and embedded Java to identify the challenging problems that still need to be solved in order to assure the success of real-time Java as a technology, and to report results and experiences gained by researchers.

Workshops organized and funded in Y4 (3/5)

- **IRTAW-15**
  September 14-16, 2011 Liébana (Cantabria), Spain
  The 15th International Real-Time Ada Workshop (IRTAW-15) will take place on September 14-16 of 2011 in Liébana (Cantabria), Spain, a nice mountain area by the "Picos de Europa" National Park.

- **WCET 2011**
  July 5th, 2011 Porto, Portugal (in conjunction with ECRTS)
  Reliable WCET bounds are a necessary component for the construction and verification of dependable real-time systems. They are an input for doing task CPU allocation, creating task schedules, and performing schedulability analysis.

- **RTN'2011**
  July 5th, 2011 Porto, Portugal (in conjunction with ECRTS 2011)

- **Map2MPSoC 2011**
  June 28-29, 2011 St. Goar, Germany
  The aim of the workshop is to provide a forum for brainstorming and road-mapping the future of mapping applications to MPSoCs. Knowledge about constraints and directions for future MPSoC architectures should be collected. Existing mapping techniques should be briefly presented and analyzed. Directions for future research should be proposed and evaluated.

- **UML&FM'2011**
  *June 20th, 2011 Lero, Limerick, Ireland (*FM 2011)
  The UML and formal methods communities have been working for a number of years to produce a practical (via UML) and rigorous (via formal methods) approach to software engineering.

Workshops organized and funded in Y4 (4/5)

- **UML&AADL'2011**
  April 27th, 2011 Las Vegas, USA (in conjunction with ICECCS 2011)
  Sixth IEEE International workshop UML and AADL

- **APRES 2011**
  April 11th, 2011 Chicago, USA (within CPS Week 2011)
  Adaptive embedded systems can respond to environmental changes including hardware/software defects, resource changes, and non-continual feature usage. As such, adaptive systems can extend the area of operations and improve efficiency in the use of system resources. However, adaptability also incurs overhead in terms of system complexity and resource requirements. For example, an adaptive system requires some means for reconfiguration. These means and their mechanisms introduce additional complexity to the design and the architecture of the system, at the same time require additional resources such as computation, power, and communication bandwidth. Consequently, adaptive systems must be diligently planned, designed, analyzed, and built to find the right tradeoffs between flexibility and complexity.

- **Rigorous Embedded Design 2011**
  April 10th, 2011 Salzburg, Austria (within EuroSys 2011)
  The objective of the workshop is to discuss new methodologies for the rigorous design of embedded systems. Through a series of invited talks, the workshop will survey some of the challenges and emerging approaches in the area. A series of design flows will be presented. The workshop will mainly discuss performance analysis, correctness (high confidence and security), code generation, and modeling aspects (including timed scheduling and software/hardware interactions). Those concepts shall be illustrated with examples coming from the aeronautic, automotive, and robotic areas. Interactions

Workshops organized and funded in Y4 (5/5)

- **PPES 2011**
  March 18th, 2011 Grenoble, France (within DATE)
  The PPES workshop is concerned with critical hard real-time systems that have to satisfy both efficiency and predictability requirements. For example, an electronic controller for a safety-critical system in an automobile needs to react not only correctly to external inputs such as rapid deceleration or loss of grip, but also provably within a given time-span.

- **ArtistDesign Workshop on Real-Time System Models for Schedulability Analysis**
  February 7-8, 2011 Santander, Spain
  The main objective of this Workshop was to discuss existing models of real-time systems that focus on representing the timing behaviour and requirements from the perspective of the ability to use schedulability analysis tools. By discussing the limitations of existing models it is possible to propose extensions that can fill the gaps that are required to cover state-of-the-art hardware platforms, operating systems, and scheduling techniques used in practice to develop real-time applications.

- **Synchronous Programming of Device Drivers for Global Resource Control in Embedded Operating Systems**
  January 27th, 2011 Lyon, France
  In embedded systems, controlling a shared resource like the bus, or improving a property like power consumption, may be hard to achieve when programming device drivers individually. There is a need for global resource control, taking decisions based on a centralized view of the devices' states. In this presentation, we study power consumption in sensor networks, where the nodes are small embedded systems powered by batteries.

ARTIST Summer Schools in Europe

2011

2010

2009

2008

The ArtistDesign European Network of Excellence on Embedded Systems Design has organized the 7th edition of it's highly successful "ARTIST Summer School in Europe", September 4-9th 2011 - funded by the European Commission). This is the seventh edition of yearly schools on embedded systems design, and is meant to be exceptional in terms of both breadth of coverage and invited speakers.

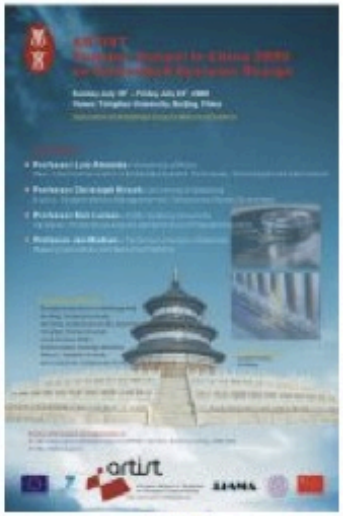
These are the largest such schools in the world for embedded systems design (120 participants in 2010), and each year gather some of the top researchers from Europe, the USA, and Asia.

This school brings together some of the best lecturers from Europe, USA and China in a 6-day programme, and was a fantastic opportunity for interaction. The 2011 edition was held in beautiful Aix-les-Bains, near Grenoble - France.

ARTIST Summer Schools in China

2011

2010



2009

2008

The ArtistDesign European Network of Excellence on Embedded Systems Design has organized the highly successful "ARTIST Summer School in China" series - funded by the European Commission. 2011 was the sixth edition of these yearly schools on embedded systems design.

The school was open in priority to Chinese students. We believe that this will open opportunities for collaboration with Chinese research teams. The school offers a full week consisting of in-depth tutorials on state-of-the-art techniques for the design and analysis of embedded systems given by leading experts.

We aimed to provide a forum for young professors, lecturers, researchers, postgraduates (advanced master and PhD students) working in embedded systems as well as engineers from industry with practical background with the development of embedded systems.

ARTIST Summer School in South America

The

2009

2008

2007

ARTIST Summer School in South America has strengthened the cooperation between Europe and South America in the area of embedded systems, both at educational and research levels. For this purpose, the goal of the school is to provide state-of-the-art courses on embedded systems oriented towards advanced students and young researchers. It should also provide a pleasant atmosphere for research-related discussions among the participants.

2010

Read more ...

top of page

ARTIST Summer School in Morocco - 2010

In collaboration with ENSIAS, the ARTIST NoE has organized a summer school on Embedded Systems in July 2010. This school, given by distinguished ARTIST lecturers, offered a full week consisting of five in-depth tutorials on state-of-the-art techniques for the design and analysis of embedded and real-time systems and networks.

The school will be held at the Ecole Nationale Supérieure d'Informatique et d'Analyse des Systèmes (ENSIAS) in Rabat, Morocco.

2010

ARTIST Summer School in Europe 2011

Slots are 2 hours each. Lunch is 12:30-14:00 on-site.

	Sunday	Monday	Tuesday	Wednesday	Thursday	Friday
9:00	**Janos Sztipanovits** *Domain Specific Modeling Languages for Cyber Physical Systems: Where are Semantics Coming From?*	**Fabien Clermidy** *Designing Network-on-Chip based multi-core heterogeneous System-on-Chip: the MAGALI experience*	**Luca Benini** *Managing MPSoCs beyond their Thermal Design Power*	**Alberto Sangiovanni** *Mapping abstract models to architectures: automatic synthesis across layers of abstraction*	**Lothar Thiele** *Temperature-aware Scheduling*	10:00 **Yunhao Liu** *GreenOrbs: Lessons Learned from Extremely Large Scale Sensor Network Deployment*
11:00	break	break	break	break	break	12:00
11:30	**Rastislav Bodik** *Automatic Programming*	**Sanjoy Baruah** *Certification-cognizant scheduling*	**Round Table** *topic tbd*	**Babak Falsafi** *Towards Dark Silicon*	**Tarek Abdelzaher** *Challenges*	*buffet lunch*
12:30	*buffet lunch*	*buffet lunch*	*buffet lunch*	*BBQ lunch*	*buffet lunch*	*Chartered buses will leave for Geneva and Lyon airports just after lunch.*
14:00	*Revisited*	*in integrated computing environments*	*Informal discussions / Afernoon in Annecy (optional) / On-site activities (optional)*	*and its Implication on Server Design*	*in Human-centric Sensor Networks*	
15:00	break	break		break	break	
15:30	**Kim Larsen** *Timing and Performance Analysis of Embedded Systems*	**Peter Druschel** *Trust and Accountability in Social Systems*		**Rolf Ernst** *Mixed safety critical system design and analysis*	**Martti Forsell** *Parallelism, programmability and architectural support for them on multi-core machines*	
17:30						
	Dinner *on-site*	**Dinner** *on-site*	*Gala Dinner:* **Dinner on the lake of Annecy** *touring on a boat*	**Dinner** *on-site*	**Farewell Buffet Dinner** with live jazz *on-site*	

Graduate Courses in Y4

- **ARTIST Quantitative Model Checking Winter School 2012**
  February 27th - March 1st 2012 Copenhagen, Denmark

Graduate Courses in Y4

- **Time-Predictable and Composable Architectures for Dependable Embedded Systems**
  October 9th, 2011 Taipei, Taiwan

  Embedded systems must interact with their real-time environment in a timely and dependable fashion. Most embedded-systems architectures and design processes consider "non-functional" properties such as time, energy, and reliability as an afterthought, when functional correctness has (hopefully) been achieved. As a result, embedded systems are often fragile in their real-time behaviour, and take longer to design and test than planned. Several techniques have been proposed to make real-time embedded systems more robust, and to ease the process of designing embedded systems, including

  Precision-timed and time-triggered architectures, to make time a first-class citizen of system design. Deterministic architectures for repeatable timing behaviour. Composability, which guarantees that the (non)-functional behaviour of components is unchanged on integration in a larger system.

  The aim of this tutorial is to present the state of the art and major approaches to time-predictability and composability, such as BIP, TTA, PRET, PTIDES, Giotto, TipToe, and CompSOC.

Graduate Courses in Y4

- **ARTIST Summer School on ICT for Future Energy Systems 2011**
  July 25-29, 2011 Povo, Trento, Italy

  Reducing buildings overall energy consumption, providing smarter power grids and optimizing industrial processes will play a key role in Future Energy Systems.

  The school will show how distributed and pervasive sensing, monitoring and control are exploited to achieve this goal and will pose novel research challenges in the development of distributed applications related to generation, storage and efficient use of energy sources.

Graduate Courses in Y4

ARTIST Graduate School on RT Kernels for Microcontrollers – 2011
June 13-17, 2011 Pisa, Italy

The course has two main objectives:

- Introducing the most important concepts and methodologies used to develop a real-time embedded system, including fundamentals of real-time scheduling, control and distributed systems;
- Showing how to apply these concepts in practice, using an embedded platform and a real-time operating system to developed simple control applications and make experience with wireless sensor networks.

Organized with ARTIST Partners in Y4

SOMRES 2011
November 29th, 2011 *Vienna, Austria*

WATERS 2011
July 5th, 2011 *Porto, Portugal*

SCOPES 2011
June 28-29, 2011 *Schloss Rheinfels, St. Goar, Germany*

UML&FM'2011
June 20th, 2011 *Lero, Limerick, Ireland (FM 2011)*

ICE 2011
June 9th, 2011 *Reykjavik, Iceland (satellilte of DisCoTec'11)*

UML&AADL'2011
April 27th, 2011 *Las Vegas, USA (in conjunction with ICECCS 2011)*

WRiSE 2011
April 10th, 2011 *Salzburg, Austria (within EuroSys 2011)*

MoBE-RTES 2011
March 28th, 2011 *Newport Beach, California*

ArtistDesign Research Topics

- Modeling and Validation
- SW Synthesis, Code Generation and Timing Analysis
- Operating Systems and Networks
- Hardware Platforms and MPSoC Design
- Intercluster activity: Design for Adaptivity
- Intercluster activity: Design for Predictability and Performance

Upcoming Artist Events

- UML&AADL'2010 *March 24th, 2010*
- CPS Week 2010 *April 12-16, 2010*
- GREEMBED 2010 *April 12th, 2010*
- FESA 2010 *April 12th, 2010*
- WARM 2010 *April 12th, 2010*
- EuroSys 2010 *April 13-16, 2010*
- SCOPES 2010 *June 28-29, 2010*
- Mapping Applications to MPSoCs 2010 *June 29-30, 2010*
- WCET 2010 *July 6th, 2010*
- OSPERT 2010 *July 6th, 2010*

ARTEMIS / ARTEMISIA

- ARTEMIS European Technology Platform
- Strategic Research Agenda
- ARTEMISIA Industrial Association

Hot Topics

- Smart and Efficient Energy Council (SEEC'2009) PRESS RELEASE
- WCET Special Issue
- Guide to Embedded Systems Concepts
  Common Technical Baseline

ACM SIGBED

- ACM Special Interest Group on Embedded Systems
  - Publications
  - Events
  - Membership

Other ES Links

- Journals
- Conferences
- Hot Topics
- Standards
- Tools and Platforms
- Main Projects
- Position Papers
- Roadmaps
- Newsletters and Magazines
- Mainstream Press
- Announcements
- Publications

Schools & Seminars

- 1st AVACS Spring School *March 15-19, 2010*

WS & Conferences

- UML&AADL'2010 *March 24th, 2010*
- CPS Week 2010 *April 12-16, 2010*
- GREEMBED 2010 *April 12th, 2010*
- FESA 2010 *April 12th, 2010*
- WARM 2010 *April 12th, 2010*
- EuroSys 2010 *April 13-16, 2010*
- MoBE-RTES 2010 *May 4th, 2010*
- ECRTS'10 *June 6-9, 2010*
- ICE'10 *June 10th, 2010*
- SCOPES 2010 *June 28-29, 2010*
- Mapping Applications to MPSoCs 2010 *June 29-30, 2010*
- WCET 2010 *July 6th, 2010*
- OSPERT 2010 *July 6th, 2010*
- SIES 2010 *July 7-9, 2010*
- DSD 2010 - 13th Euromicro *September 1-3, 2010*
- ICT 2010 *September 27-29, 2010*

SEVENTH FRAMEWORK PROGRAMME

Subscriptions

- ARTIST Mailing List

Past Events

Organised by Artist

- UML&FM'2009 *December 8th, 2009*
- WESH 2009 *December 7th, 2009*
- CRTS 2009 *December 1st, 2009*
- WSS'09 *October 16th, 2009*
- WESE'09 *October 15th, 2009*
- RePP 2009 *October 15th, 2009*
- WFCD - Foundations and Applications of Component-based Design 2009 *October 11th, 2009*
- APRES'09 *October 11th, 2009*
- SEEC'09 *October 8-9, 2009*
- IRTAW-14 *October 7-9, 2009*
- ACES[MB] 2009 *October 6th, 2009*
- VVPS 2009 *September 19-20, 2009*
- ARTIST Summer School in Europe 2009 *September 7-11, 2009*
- ARTIST School in South America 2009: Embedded Systems Design *August 3-7, 2009*
- ARTIST Summer School in China 2009 *July 19-24, 2009*
- WCET 2009 *June 30th, 2009*
- OSPERT 2009 *June 30th, 2009*
- Mapping Applications to MPSoCs 2009 *June 29-30, 2009*
- Runtime Verification 2009 *June 26-28, 2009*
- ArtistDesign NoE - Embedded Systems Seminar *June 18-19, 2009*
- ARTIST Graduate Course: Automated Formal Methods for Embedded Systems - 2009 *June 17-25, 2009*
- ARTIST Graduate Course on Embedded Control Systems 2009 *June 8-12, 2009*
- DySCAS 2009 *February 18th, 2009*
- Mapping of Applications-to MPSoCs - ArtistDesign Working Meeting *November 27-28, 2008*
- Embedded Systems: Industrial Applications '08 *November 12-13, 2008*
- WS on Multicores: Theory and Practice *October 28th, 2008*
- UML&FM'08 *October 27th, 2008*
- WESE'08: WS on Embedded Systems Education

Sponsored by Artist

- NWPT '09 *October 14-16, 2009*
- ESWeek 2009 *October 11-16, 2009*
- CAV 2009 *June 26th - July 2nd 2009*
- UML&AADL'2009 *June 2nd, 2009*
- MDD for Distributed Real-time Embedded Systems (MDD4DRES) 2009 *April 20-24, 2009*
- DATE 2009 *April 20-24, 2009*
- FeBID 2009 *April 16th, 2009*
- HSCC 2009 *April 13-15, 2009*
- RNTS'08 *October 16-17, 2008*
- DATE'08 *March 10-14, 2008*
- EmSoft'07 *October 1-3, 2007*
- Embedded Systems Week 2007 *September 30th - October 5th 2007*
- EPSD 2007 *September 10-14, 2007*
- FOSAD 2007 *September 9-15, 2007*
- UML&AADL'2007 *July 14th, 2007*
- CAV 2007 *July 3-7, 2007*
- FMGALS'2007 *May 29th, 2007*
- SCOPES 2007 *April 20th, 2007*
- HSCC'07 *April 3-5, 2007*
- SLA++P 2007 *March 31st, 2007*
- ARCS 2007 *March 12-15, 2007*
- CASTNESS'07 Workshop and School *January 15-17, 2007*
- CASTNESS'07 Workshop and School *January 15-17, 2007*
- Synchron 2006 *November 27th - December 1st 2006*
- JTRES 2006 *October 11-13, 2006*
- MARTES 2006 *October 2nd, 2006*
- ADSD 2006: Advanced Digital Systems Design *September 25-29, 2006*
- FOSAD 2006: 6th International School on Foundations of Security Analysis and Design *September 10-16, 2006*
- Workshop: Distributed Embedded Systems *November 21-24, 2005*
- OSPERT 2005 *July 5th, 2005*
- HSCC '05 - Hybrid Systems: Computation and Control

Organised with Artist Partners

- CODES+ISSS 2009 *October 15-16, 2009*
- RePP 2009 *October 15th, 2009*
- NWPT '09 *October 14-16, 2009*
- ESWeek 2009 *October 11-16, 2009*
- Multiparadigm Modeling 2009 *October 4-9, 2009*
- SAMOS IX *July 20th - January 23rd 2009*
- SIES 2009 *July 8-10, 2009*
- ECRTS 2009 *July 1-3, 2009*
- LCTES'09 *June 19-20, 2009*
- UML&AADL'2009 *June 2nd, 2009*
- SCOPES 2009 *April 23-24, 2009*
- MDD for Distributed Real-time Embedded Systems (MDD4DRES) 2009 *April 20-24, 2009*
- DATE 2009 *April 20-24, 2009*
- FeBID 2009 *April 16th, 2009*
- Cyber Physical Systems Week 2009 *April 13-16, 2009*
- HSCC 2009 *April 13-15, 2009*
- CiberMouse@RTSS2008 *November 30th, 2008*
- ESWEEK 2008 *October 19-24, 2008*
- RNTS'08 *October 16-17, 2008*
- ECRTS 2008 *July 2-4, 2008*
- Ada-Europe'08 *June 16-20, 2008*
- Cyber Physical Systems Week 2008 *April 21-24, 2008*
- DATE'08 *March 10-14, 2008*
- CASTNESS 2008 *January 15-18, 2008*
- RTSS 2007 *December 3-6, 2007*
- FORMATS'07 *October 3-5, 2007*
- EmSoft'07 *October 1-3, 2007*
- CODES-ISSS 2007 *September 30th - October 5th 2007*
- RTCSA 2007 *August 21-24, 2007*
- UML&AADL'2007 *July 14th, 2007*
- ECRTS 2007 *July 4-6, 2007*
- WCET'07 *July 3rd, 2007*

ADSIG

Who we are

ARTIST NoE

- Workshops
- Schools and seminars
- International Collaboration
- Education
- Mailing List

How to reach us

Who we are

The ADSIG (ArtistDesign Special Interest Group) has been created as a followup to the ARTIST European Network of Excellence on Embedded System Design, to serve as a focal point for the vibrant community it has fostered.

ADSIG serves as a platform for information, contacts, events, and links within the larger Embedded Systems research community, by:

- leveraging the ArtistDesign NoE results
- supporting the advancement of state of the art embedded systems design approaches
- facilitating the exchange of ideas and knowledge in embedded systems design
- dissemination (eg: web hosting for events), projects
- supporting the creation of project consortia
- providing visibility as an entity of experts in embedded systems design
- spreading knowledge in embedded systems design
- supporting the application of embedded systems technology

It is managed by a Strategic Management Board, which decides on activities and actions, and the overall goals and policy of the SIG. The interim chair is Prof. Peter Marwedel. Operational management for ADSIG is carried out by a private company called SAXEL.

ADSIG is a part of EDAA (the European Design and Automation Association), the permanent structure that also handles the DATE conference.

 Sign In